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**FACSIMILE TRANSMISSION****CONFIDENTIAL****DATE:** July 17, 2003**CLIENT-MATTER No.:** 18602-06513 (P1975R1)**To:**

NAME	FAX NO.	PHONE NO.
Examiner Daniel G. Mariam Group Art Unit 2621 Commissioner for Patents	(703) 872-9314	(703) 305-4010

**FROM:** Kirk A. Gottlieb**PHONE:** (415) 875-2414

NUMBER OF PAGES WITH COVER PAGE: 4	ORIGINAL WILL NOT FOLLOW
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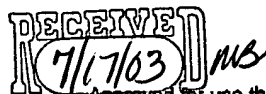
**MESSAGE:****Response**

Applicants: Eric C. Anderson *et al.*  
App. No.: 09/990,869  
Filing Date: November 21, 2001  
Title: MODULAR DIGITAL IMAGE PROCESSING VIA AN IMAGE PROCESSING CHAIN  
WITH MODIFIABLE PARAMETER CONTROLS  
Atty. Dkt. No. 18602-06513

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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

0001/PTO Rev. 10/95	U.S. Department of Commerce Patent and Trademark Office	Application Number	09/990,869
<b>TRANSMITTAL FORM</b>  (to be used for all correspondence during pendency of filed application)		Filing Date	November 21, 2001
		First Named Inventor	Eric C. Anderson et al.
		Group Art Unit Number	2621
		Examiner Name	Daniel G. Mariani
Total Number of Pages In This Submission	3	Attorney Docket Number	18602-06513 (P1975R1)

**ENCLOSURES (check all that apply)**

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REMARKS:

**SIGNATURE OF ATTORNEY OR AGENT**

Signature:			
Attorney/Reg. No.:	Kirk A. Gottlieb, Reg. No. 42,596	Dated:	July 17, 2003

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Typed or Printed Name:	Kirk A. Gottlieb	Dated:	July 17, 2003
Facsimile Number:	1-703-872-9314		

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APPLICATION NO.: 09/990,869

FILING DATE: November 21, 2001

TITLE: MODULAR DIGITAL IMAGE PROCESSING VIA AN IMAGE PROCESSING  
CHAIN WITH MODIFIABLE PARAMETER CONTROLS

EXAMINER: Daniel G. Mariam

GROUP ART UNIT: 2621

ATTY. DOCKET NO.: 18602-06513 (P1975R1)

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Dated: July

17, 2003

By:

Kirk A. Gottlieb, Reg. No. 42,596

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RESPONSE

Sir:

In response to the Examiner's telephone request of July 16, 2003, included herein is reformatted new claim 38.

IN THE CLAIMS

Please amend new claim 38 as indicated below.

38. (Amended) A system for controlling the processing of digital image data, comprising:  
a digital image capture device having an image processing backplane for forming an  
image processing chain with a plurality of image processors, the plurality of  
image processors being software modules stored in memory located within the

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